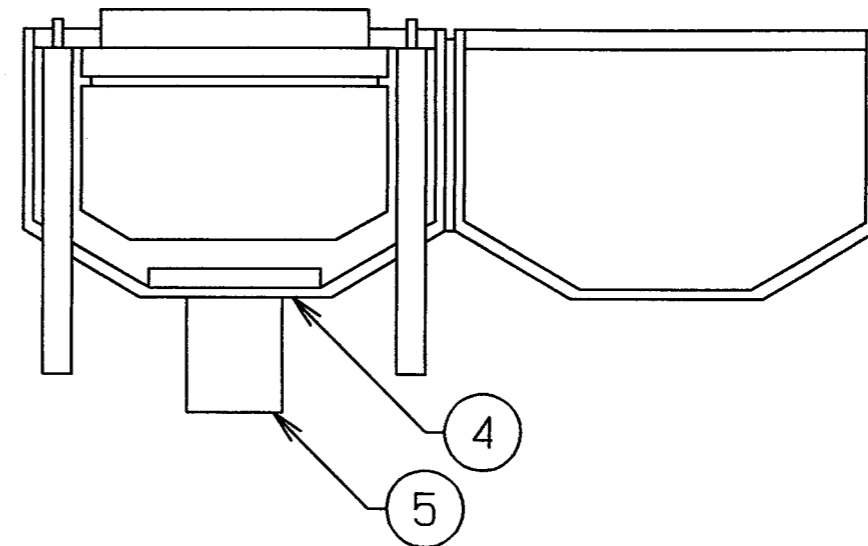
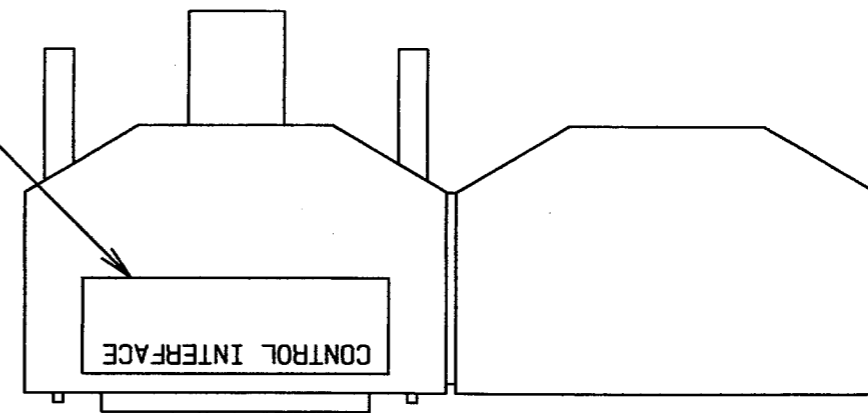


FIT LABEL, ITEM 2- AS SHOWN



INSERT WIRES FROM SIDE 1 OF PCB

ASSEMBLY DETAIL

APPLICATION NOTE:

THE FOLLOWING IS REQUIRED WITHIN THE IPC-500B OR EARLIER SERIES INTERCONNECT CHASSIS:

1. FOR NORMAL CHANNEL SELECTION (CHL1...15), FIT LINK JP6.
2. FOR OFFSET CHANNEL SELECTION (CHL81...95), REMOVE LINK JP6.
3. J7/11 MUST BE CONNECTED TO J7/25.

PCB 07-01723 ISSUE A
ASSY P/No. 08-05208-001

DISK FILE
07\1723_A.PCB

MASTER

		SCALE 2:1	COPYRIGHT © CODAN PTY LTD A.C.N. 007 590 605 CABLE & CONTROL INTERFACE PCB ASSY, IPC500 FOR 8570, 9323 & 9360			
DRN. ILT CHKD. JW APPD. AMJ	DATE 27-7-95 25-8-95 13-03-96	DRAWING/DOC No. 08-05208	ISS 1			
TOLERANCES UNLESS OTHERWISE STATED 2 PLACES DEC 1 PLACE DEC ANGULAR		SHT 1				